

PRIOR ART FIG. 8

The diagram illustrates a prior art system architecture. At the top, an optical input section includes a lens (1), an iris (2), and two image elements (3, 4) each associated with a motor (5, 6). Light from these elements passes through an image pickup element drive device (7) and an ND filter drive device (8) to reach a CCD sensor (10). The CCD sensor is connected to a CDS/AGC block (12), which then feeds into an A/D converter (13). The A/D converter outputs to a DSP (Digital Signal Processor) block (14). The DSP is bidirectionally connected to a MEMORY block (15) and a RECORDING MEDIUM block (16). Additionally, the DSP is connected to a MICROCOMPUTER block (17). The MICROCOMPUTER controls the ND FILTER DRIVE DEVICE (8) and the IRIS OPERATION DRIVE DEVICE (9), which in turn controls the IRIS (2).

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FIG. 9A FIG. 9B FIG. 9C FIG. 9D FIG. 9E

